



JST01 Series 1A TRIACs

Rev.1.0

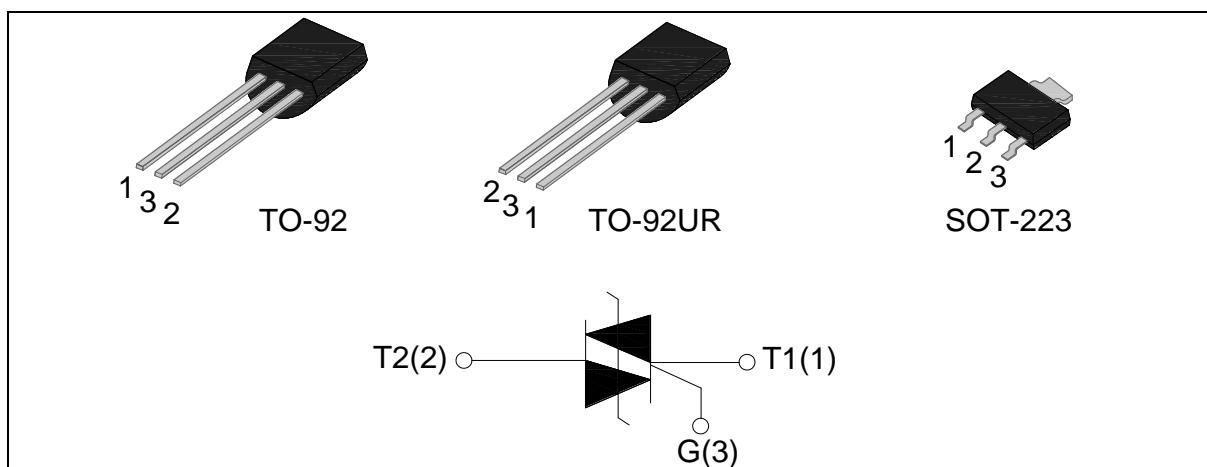
DESCRIPTION:

JST01 series provide high dv/dt rate with strong resistance to electromagnetic interface.

They are especially recommended for use on home appliances such as motor control of washing machine, and for use on industrial control systems like electromagnetic valves.

MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	1	A
I_{GT1-3}	10	mA
V_{DRM}/V_{RRM}	800 and 1000	V



ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	°C
Operating junction temperature range	T_j	-40-125	°C
Repetitive peak off-state voltage($T_j=25^\circ\text{C}$)	V_{DRM}	800/1000	V
Repetitive peak reverse voltage($T_j=25^\circ\text{C}$)	V_{RRM}	800/1000	V
Non repetitive surge peak Off-state voltage	V_{DSM}	$V_{DRM}+100$	V
Non repetitive peak reverse voltage	V_{RSM}	$V_{RRM}+100$	V
RMS on-state current	$I_{T(RMS)}$	1	A
TO-92/ TO-92UR ($T_c=57^\circ\text{C}$)			
SOT-223($T_c=90^\circ\text{C}$)			

Non repetitive surge peak on-state current (full cycle, F=50Hz)	I _{TSM}	15	A
I ² t value for fusing (tp=10ms)	I ² t	1.25	A ² s
Rate of rise of on-state current (I _G =2xI _{GT})	dI/dt	50	A/μs
Peak gate current	I _{GM}	1	A
Average gate power dissipation	P _{G(AV)}	0.1	W
Peak gate power	P _{GM}	0.5	W

ELECTRICAL CHARACTERISTICS (T_j=25°C unless otherwise specified)

Symbol	Test Condition	Quadrant		Value	Unit
I _{GT}	V _D =12V R _L =33Ω	I - II -III	MAX	10	mA
V _{GT}		I - II -III	MAX	1.3	V
V _{GD}	V _D =V _{DRM} T _j =125°C R _L =3.3KΩ	I - II -III	MIN	0.2	V
I _L	I _G =1.2I _{GT}	I -III	MAX	20	mA
		II		40	
I _H	I _T =100mA		MAX	15	mA
dV/dt	V _D =2/3V _{DRM} Gate Open T _j =125°C		MIN	200	V/μs

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX)	Unit
V _{TM}	I _{TM} =2A tp=380μs	T _j =25°C	1.5	V
I _{DRM}	V _D =V _{DRM} V _R =V _{RRM}	T _j =25°C	5	μA
I _{RRM}		T _j =150°C	1	mA

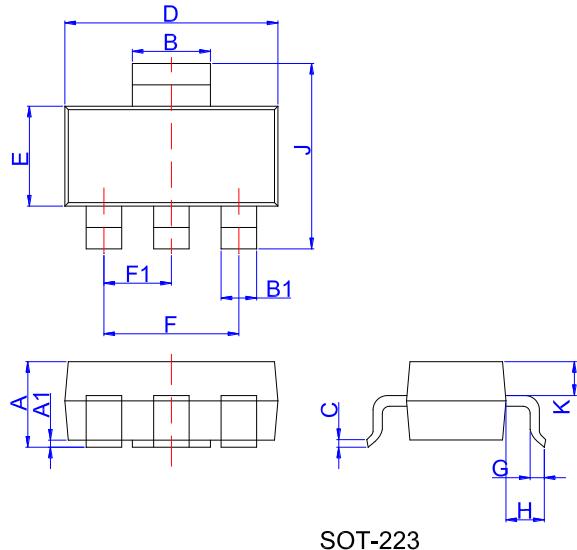
THERMAL RESISTANCES

Symbol	Parameter		Value	Unit
R _{th(j-c)}	junction to case(AC)	TO-92/ TO-92UR	23	°C/W
		SOT-223	9.5	

ORDERING INFORMATION

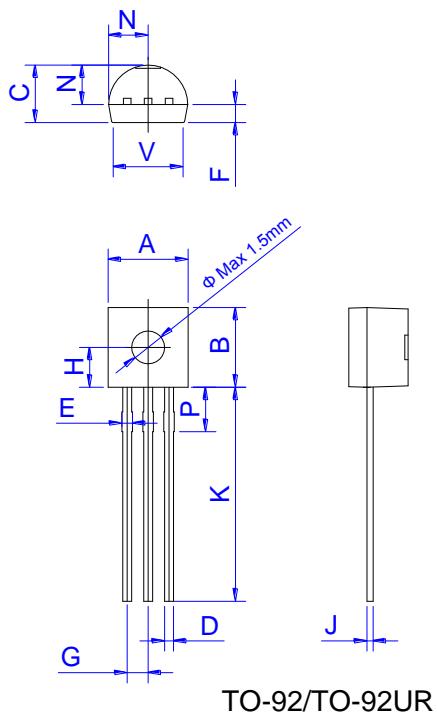
J	ST	01	U	-800	SW
JieJie Microelectronics Co.,Ltd					IGT1-3≤10mA
					800:V _{DRM} /V _{RRM} ≥800V
					1000:V _{DRM} /V _{RRM} ≥1000V
			U: TO-92		
			V: SOT-223		
			UR: TO-92UR		

PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	1.5	1.6	1.8	0.059	0.063	0.071
A1	0	0.06	0.10	0	0.002	0.004
B	2.9	3.0	3.1	0.114	0.118	0.122
B1	0.6	0.7	0.8	0.024	0.028	0.031
C	0.22	0.26	0.32	0.009	0.010	0.013
D	6.3	6.5	6.7	0.248	0.256	0.264
E	3.3	3.5	3.7	0.130	0.138	0.146
F		4.6			0.181	
F1		2.3			0.091	
G	0.7	0.9	1.1	0.028	0.035	0.043
H	1.5	1.75	2.0	0.059	0.069	0.079
J	6.7	7.0	7.3	0.264	0.276	0.287
K	0.8	0.9	1.0	0.031	0.035	0.039

PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.45		5.20	0.175		0.205
B	4.32		5.33	0.170		0.210
C	3.18		4.19	0.125		0.165
D	0.407		0.533	0.016		0.021
E	0.60		0.80	0.024		0.031
F	-	1.1	-	-	0.043	-
G	-	1.27	-	-	0.050	-
H	-	2.30	-	-	0.091	-
J	0.36		0.50	0.014		0.020
K	12.70		15.0	0.500		0.591
N	2.04		2.66	0.080		0.105
P	1.86		2.06	0.073		0.081
V	-		4.3	-		0.169

FIG.1: Maximum power dissipation versus RMS on-state current

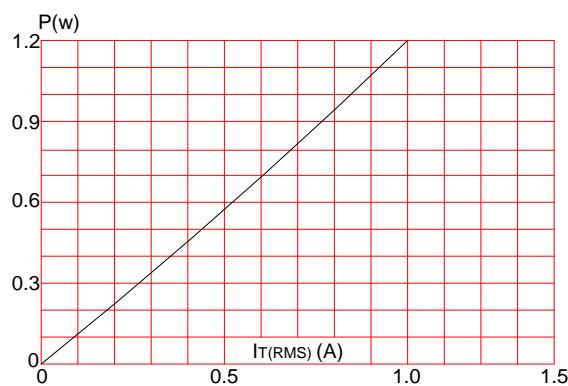


FIG.3: Surge peak on-state current versus number of cycles

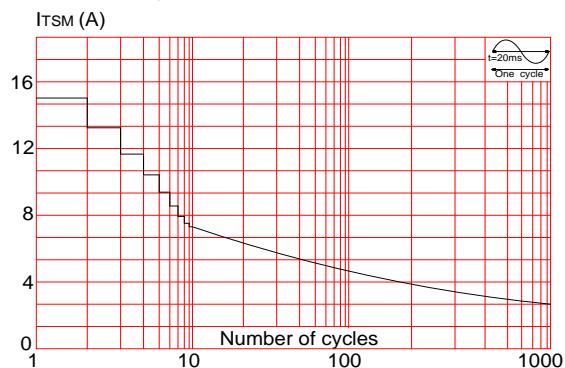


FIG.2: RMS on-state current versus case temperature

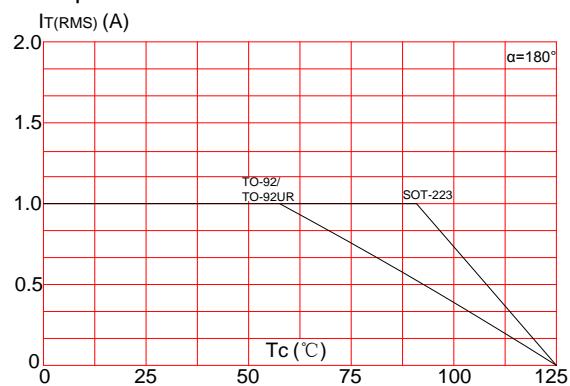


FIG.4: On-state characteristics (maximum values)

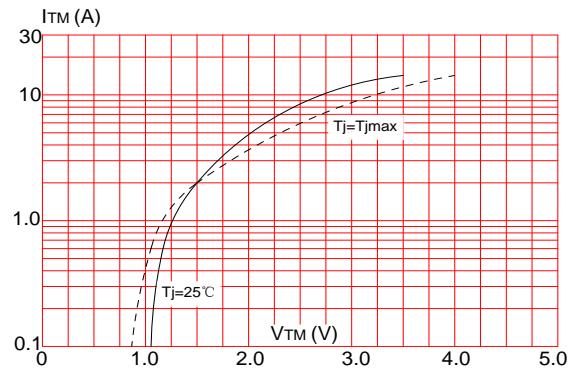


FIG.5: Relative variations of gate trigger current versus junction temperature

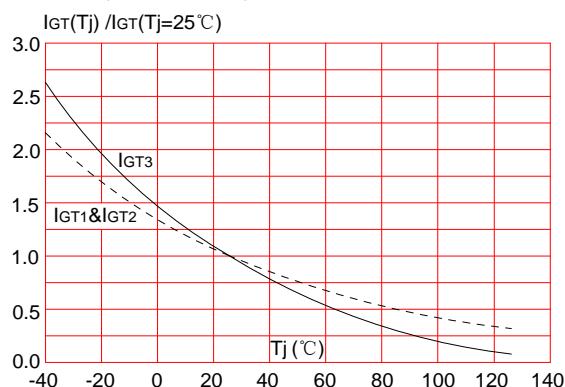
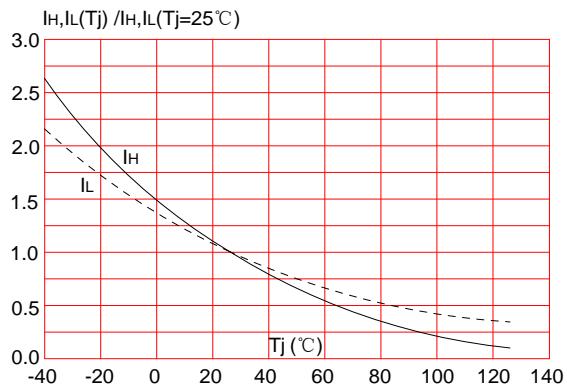


FIG.6: Relative variations of holding current, latching current versus junction temperature



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co.,Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement. Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document is the first version which is made in 11-Jan.-2016. This document supersedes and replaces all information previously supplied.

 is a registered trademark of Jiangsu JieJie Microelectronics Co.,Ltd.

Copyright©2016 Jiangsu JieJie Microelectronics Co.,Ltd. Printed All rights reserved.